

	Hits	S arch Text	DBs	Time Stamp
1	6	("5867382" "6141596" "6178403" "6185538" "6559831" "6574672").pn.	USPAT	2004/12/29 12:35
2	5514	processor near2 (control\$4 configur\$3 arrang\$3) with (switch\$2 (("I/O" (input\$loutput)) adj circuit\$1))	USPAT	2004/12/29 13:59
3	5439	processor near2 (control\$4 configur\$3 arrang\$3) with switch\$2	USPAT	2004/12/29 12:39
4	5178	processor near2 control\$4 with switch\$2	USPAT	2004/12/29 12:39
5	1636	processor near2 control\$4 near2 switch\$2	USPAT	2004/12/29 13:12
6	17	(processor near2 control\$4 near2 switch\$2) with status	USPAT	2004/12/29 12:54
7	251	processor with inputs with output with switch\$2	USPAT	2004/12/29 12:55
8	51	processor with inputs with output with switch\$2 with circuit\$1	USPAT	2004/12/29 13:11
9	0	processor with inputs with output with switch\$2 with (conditioning adj circuit\$1)	USPAT	2004/12/29 12:55
10	25	processor with (inputs near5 circuit\$1) with output with switch\$2	USPAT	2004/12/29 12:58
11	1	processor with inputs with output with switch\$2 with circuit\$1 and firmware	USPAT	2004/12/29 13:11
12	11	processor near2 control\$4 near2 switch\$2 with firmware	USPAT	2004/12/29 14:04
13	3	processor near2 (control\$4 configur\$3 arrang\$3) near3 switch\$2 with (("I/O" input\$loutput) adj circuit\$1)	USPAT	2004/12/29 14:03
14	0	processor near2 (control\$4 configur\$3 arrang\$3) near3 switch\$2 with (("I/O" input\$loutput) adj circuit\$1)	US-PGPUB; EPO; JPO; IBM_TDB	2004/12/29 14:03
15	2	processor near2 control\$4 near2 switch\$2 with firmware	US-PGPUB; EPO; JPO; IBM_TDB	2004/12/29 14:04